

## Refine Search

### Search Results -

Terms	Documents
(717/162).ccls.	161

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L10

Refine Search

Recall Text

Clear

Interrupt

### Search History

DATE: Monday, May 17, 2004    [Printable Copy](#)    [Create Case](#)

<u>Set</u> <u>Name</u> <u>Query</u> side by side	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=USPT; PLUR=YES; OP=ADJ</i>		
<u>L10</u> 717/162.ccls.	161	<u>L10</u>
<u>L9</u> 717/136,137,138,139,140.ccls.	531	<u>L9</u>
<i>DB=TDBD; PLUR=YES; OP=ADJ</i>		
<u>L8</u> source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	0	<u>L8</u>
<i>DB=DWPI; PLUR=YES; OP=ADJ</i>		
<u>L7</u> source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	1	<u>L7</u>
<i>DB=JPAB; PLUR=YES; OP=ADJ</i>		
<u>L6</u> source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	0	<u>L6</u>

*DB=EPAB; PLUR=YES; OP=ADJ*

<u>L5</u>	source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	0	<u>L5</u>
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*DB=USOC; PLUR=YES; OP=ADJ*

<u>L4</u>	source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	0	<u>L4</u>
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*DB=PGPB; PLUR=YES; OP=ADJ*

<u>L3</u>	source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$) and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	1	<u>L3</u>
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*DB=USPT; PLUR=YES; OP=ADJ*

<u>L2</u>	L1 and (relocation\$ near5 (indicator\$ or pointer\$)) and (relocation\$ near4 instruction\$)	20	<u>L2</u>
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<u>L1</u>	source code\$ and assembl\$ and relocation\$ and ((object\$ or executable\$ or machine\$)near4 code\$)	153	<u>L1</u>
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END OF SEARCH HISTORY